APPLICABLE STANDARD		DARD						
OPERATING TEMPERATUR		E RANGE	-45°C TO +125°C (NOTES 1)	STORAGE TEMPERATURE RANGE		-10°C TO + 60°C (NOTE2		2)
RATING	VOLTAGE		150V AC		LICABLE	DF9#-*P-1V(22)		
	CURRENT		0. 5A	COV	INECTOR	DF9#-*P-1V(32)		
	1		SPECIFICAT	10	NS .			
ITEM			TEST METHOD		REQUIREMENTS			АТ
CONSTR	RUCTION					•		
		VISUALLY	Y AND BY MEASURING INSTRUMENT.		ACCORDING TO DRAWING.			Х
MARKING		CONFIRMED VISUALLY.						Х
ELECTR	IC CHARA	CTERIS	STICS					
	RESISTANCE	100m A (DC OR 1000 Hz).			50mΩ MAX.			_
INSULATION RESISTANC		100V DC.			500MΩ MIN.			_
VOLTAGE P		250V AC FOR 1 min.			NO FLASHOVER OR BREAKDOWN.			
I MECHAN	NICAL CHA	RACTE	ERISTICS				Х	
MECHANICA			SINSERTIONS AND EXTRACTIONS.	① CONTACT RES	ISTANCE: 50mΩ MAX.			
OPERATION	١			② NO DAMAGE, CRACK OR LOOSENESS OF PARTS.			_	
VIBRATION		FREQUENCY 10 TO 55 Hz, SINGLE AMPLITUDE 0.75 mm, AT 2 h, FOR 3 DIRECTIONS.			① NO ELECTRICAL DISCONTINUITY OF 1μs.			_
SHOCK			DURATION OF PULSE 11 ms AT 3 TIM	IFS	 ② NO DAMAGE, CRACK OR LOOSENESS OF PARTS. ① NO ELECTRICAL DISCONTINUITY OF 1μs. 			
Si ioon		FOR 3 DIRECTIONS.			_	EK OR LOOSENESS OF PARTS.	Χ	–
		1	ENVIRONMENTAL CHA	RAC				I
RAPID CHA	NGE OF	TEMPERA	ATURE -65→ 5 TO 35→125→ 5 TO 35°C			TANCE: 50mΩ MAX.		
TEMPERAT	URE	TIME 30→10 TO 15→ 30→10TO15min UNDER 5 CYCLES.			(2) INSULATION RESISTANCE: 500 M Ω MIN. (3) NO DAMAGE, CRACK OR LOOSENESS OF PARTS.			-
DAMP HEAT		EXPOSED AT 40 ± 2 °C, 90 TO 95 %, 96 h.			① CONTACT RESISTANCE: 50mΩ MAX.			
(STEADY ST	ΓATE)	1			② INSULATION RESISTANCE: 500 MΩ MIN.			–
HEAT RESIS	STANCE OF	[RECOMMENDED TEMPERATURE PROFILE]			③ NO DAMAGE, CRACK OR LOOSENESS OF PARTS. NO DEFORMATION OF CASE OF EXCESSIVE			
SOLDERING		《SOLDERING AREA》 MAX250°C, 220°C FOR 60 SECONDS MAX. 《PREHEATING AREA》 150 TO 180°C 90∼120 SECONDS. MAXIMUM TWICE ACTION IS ALLOWED UNDER THE SAME CONDITION. 【RECOMMENDED MANUAL SOLDELING CONDITION 】 SOLDERING IRON TEMPERATURE 380°C SOLDERING TIME: WITHIN 3 SECONDS.		LOOSENESS OF THE TERMINALS.		X	_	
SOLDERABILITY		SOLDERING TEMPARATURE:245±5°C DURATION OF IMMERSION: SOLDERING FOR 3SECONDS		A NEW UNIFORM COATING OF SOLDER SHALL COVER MINIMUM OF 95% OF THE SURFACE BEING IMMERSED.			_	
REMARKS								

NOTE1:INCLUDING THE TEMPERATURE RISE BY CURRENT. NOTE2:STORAGEIS DEFINED AS LONG-TERM STORAGE OF UNUSED PRODUCTS. APPLY OPERATION TEMPERATURE RANGE TO PRODUCTS MOUNTED ON PCB WITHOUT POWER SUPLLY.

UNLESS OTHERWISE SPECIFIED , REFER TO JIS C 5402 .

	COUNT	DESCRIPTION OF REVISIONS	DESIGNED		CHECKED		D.	DATE	
	1	DIS-H-001214	AR. TAKAHASHI		TS.MIYAZAKI		06.	06. 08. 02	
			APPROVED		MO. NAKAMURA	05.	05. 10. 31		
1			CHECKED		TS. MIYAZAKI	MIYAZAKI 05.1			
1			DESIGNED		TY. 00 I 05		05. 10. 31		
			DRAWN		HK. MURAKAMI	HK. MURAKAMI 05.			
Note	QT:Qu	alification Test AT:Assurance Test X:Applicable Tes	t DRAWII	NG NO.	ELC4-162417-04				
		SPECIFICATION SHEET	PART NO.	PART NO. CODE NO.		DF9A-*S-1V(22)			
		HIROSE ELECTRIC CO., LTD.	CODE NO.			CL540		1/1	