

Surge Arrester T83-A230XF4

## 3-Electrode-Arrester

Ordering code: B88069X8870B502

DC spark-over voltage 1) 2) 4)		230 ± 20	V %
Impulse spark-over v at 100 V/µs		< 450 < 400	V
at 1 kV/µs	<ul><li>for 99 % of measured values</li><li>typical values of distribution</li></ul>	< 650 < 600	V
Nominal impulse discharge current (wave 8/20 µs) 5 Single impulse discharge current (wave 8/20 µs) 5 (wave 8/20 µs)		10 15	kA kA
Nominal alternating discharge current (50 Hz, 1 s) 5) Alternating discharge current (50 Hz, 9 cycles) 5)		10 40	A A
Response time of failsafe mechanism at 1 A, typical		< 10	s
Insulation resistance at 100 V <sub>dc</sub> <sup>4)</sup>		> 10	$G\Omega$
Capacitance at 1 MHz <sup>4)</sup>		< 1.5	pF
Transverse delay time 3)		< 0.2	μs
Arc voltage at 1 A, typical Glow to arc transition current Glow voltage		< 25 ~ 1 ~ 200	V A V
Weight		~ 2.2	g
Storage temperature		-40 +90	°C
Climatic category (IEC 60068-1)		40/ 90/ 21	
Marking, red		EPCOS 230 YY O 230 - Nominal voltage YY - Year of production O - Non radioactive	

<sup>1)</sup> At delivery AQL 0.65 level II, DIN ISO 2859
2) In ionized mode

Terms in accordance with ITU-T Rec. K.12 and DIN 57845/VDE0845

The arrester failsafe mechanism contains a solder pellet with a melting temperature between 193 and 203 °C.

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Test according to ITU-T Rec. K.12

Tip or ring electrode to center electrode

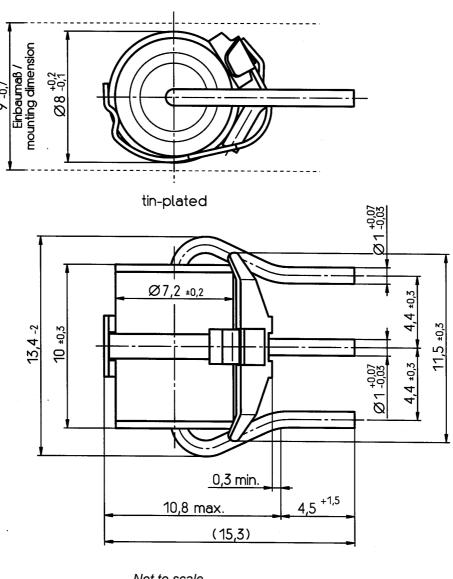
Total current through center electrode, half value through tip respectively ring electrode.



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Not to scale

Dimensions in mm

Non controlled document

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