PTC thermistors for overcurrent protection

Leaded disks, coated, 230 V, lead-free series

Series/Type: **B598****
Date: February 2012

Applications

- Overcurrent protection
- Short circuit protection

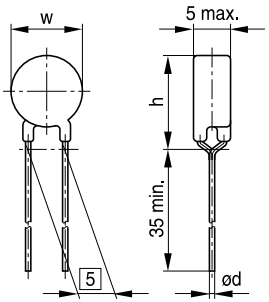
Features

- No intentional use of lead compounds
- Lead concentration in the homogenous materials lower than 0.1%
- Tight resistance tolerance
- Marking: Type, manufacturer's logo, reference temperature in °C and date code YYWW
- UL approval to UL 1434 (file number E69802)
- VDE approval for selected types (licence number 104843 E)

Delivery mode

- Cardboard strips (standard)

Dimensional drawing



TPT0648-4

Dimensions (mm)

Type	w_{max}	h_{max}	∅d
C850	13.5	17.0	0.6
C860	11.0	14.5	0.6
C873	9.0	12.5	0.6
C875	9.0	12.5	0.6
C880	6.5	10.0	0.6
C883	6.5	10.0	0.6

General technical data

Max. operating voltage	(T_A = 60 °C)	V_{max}	265	V DC or V AC
Rated voltage		V_R	230	V DC or V AC
Switching cycles		N	100	
Tolerance of R_R	(T_{ref} = 120 °C)	ΔR_R	±20	%
Reference temperature	(typ.)	T_{ref}	120	°C
Operating temperature range	(V = 0)	T_{op}	−40/+125	°C
Operating temperature range	(V = V_{max})	T_{op}	0/+60	°C

Electrical specifications and ordering codes

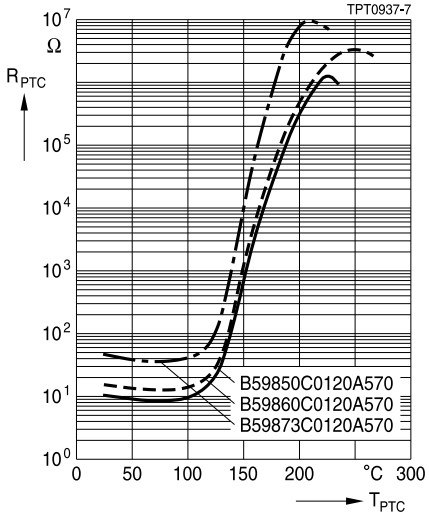
Type	I_R	I_S	I_{Smax} (V = V_{max})	I_r (typ.) (V = V_{max})	T_{ref} (typ.)	R_R	R_{min}	Approvals		Ordering code
	mA	mA	A	mA	°C	Ω	Ω			
C850	220	330	2.2	13	120	10	6	X	–	B59850C0120A570
C860	170	260	1.5	10	120	15	9	X	–	B59860C0120A570
C873	90	140	1.0	9	120	45	27	X	–	B59873C0120A570
C875	80	120	1.0	9	120	65	39	X	–	B59875C0120A570
C880	65	98	0.4	6	120	70	42	X	X	B59880C0120A570
C883	50	75	0.4	5	120	120	72	X	X	B59883C0120A570

Reliability data

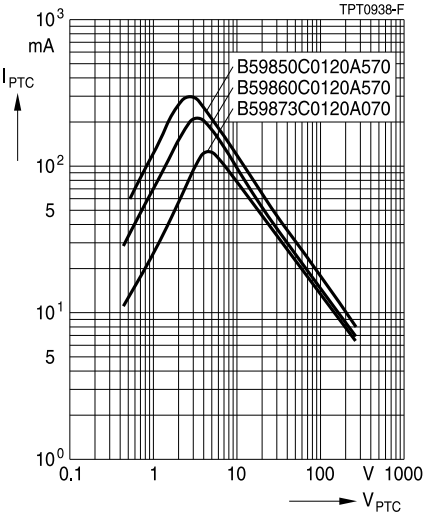
Test	Standard	Test conditions	$\lvert\Delta R_{25}/R_{25}\rvert$
Electrical endurance, cycling	IEC 60738-1	Room temperature, I_{Smax}; V_{max} Number of cycles: 100	< 25%
Electrical endurance, constant	IEC 60738-1	Storage at V_{max} /$T_{op,max}$ (V_{max}) Test duration: 1000 h	< 25%
Damp heat	IEC 60738-1	Temperature of air: 40 °C Relative humidity of air: 93% Duration: 56 days Test according to IEC 60068-2-78	< 10%
Rapid change of temperature	IEC 60738-1	$T_1 = T_{op,min}$ (0 V), $T_2 = T_{op,max}$ (0 V) Number of cycles: 5 Test duration: 30 min Test according to IEC 60068-2-14, test Na	< 10%
Vibration	IEC 60738-1	Frequency range: 10 to 55 Hz Displacement amplitude: 0.75 mm Test duration: 3 × 2 h Test according to IEC 60068-2-6, test Fc	< 5%
Shock	IEC 60738-1	Acceleration: 390 m/s^2 Pulse duration: 6 ms; 6 × 4000 pulses	< 5%
Climatic sequence	IEC 60738-1	Dry heat: T = $T_{op,max}$ (0 V) Test duration: 16 h Damp heat first cycle Cold: T = $T_{op,min}$ (0 V) Test duration: 2 h Damp heat 5 cycles Tests performed according to IEC 60068-2-30	< 10%

Characteristics (typical)

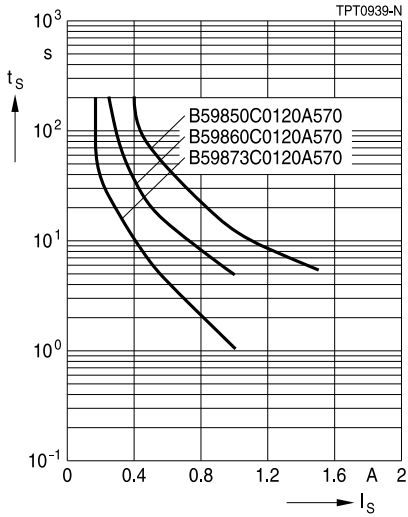
PTC resistance R_{PTC} versus PTC temperature T_{PTC} (measured at low signal voltage)



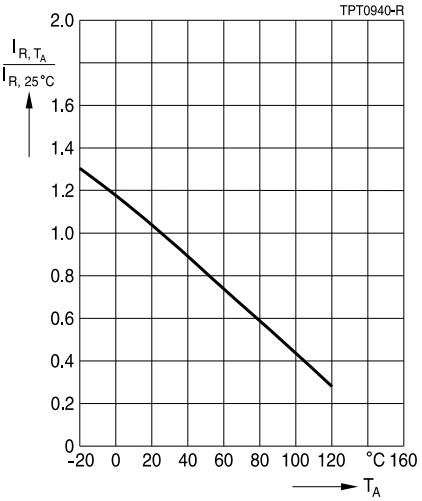
PTC current I_{PTC} versus PTC voltage V_{PTC} (measured at 25 °C in still air)



Switching time t_S versus switching current I_S (measured at 25 °C in still air)

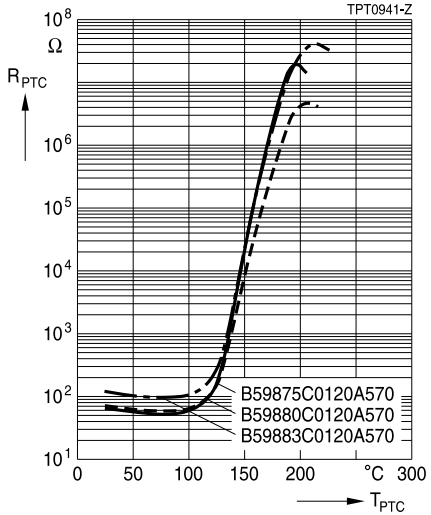


Rated current I_R versus ambient temperature T_A (measured in still air)

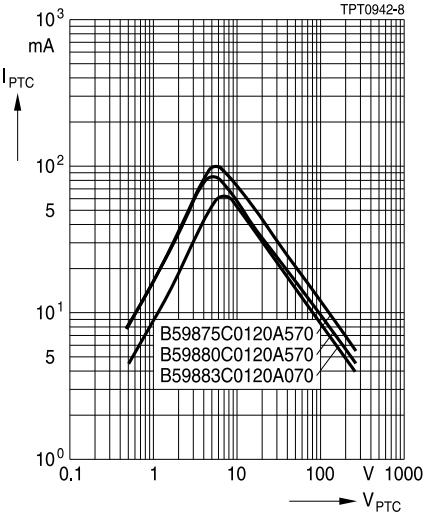


Characteristics (typical)

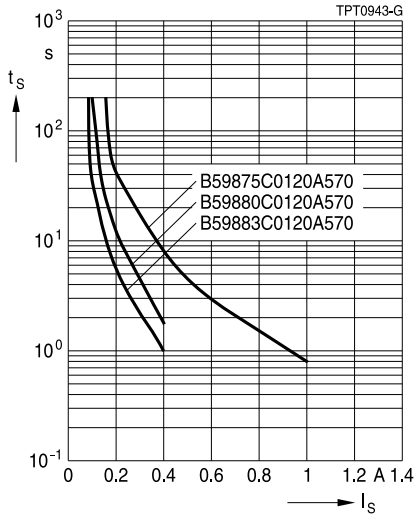
PTC resistance R_{PTC} versus PTC temperature T_{PTC} (measured at low signal voltage)



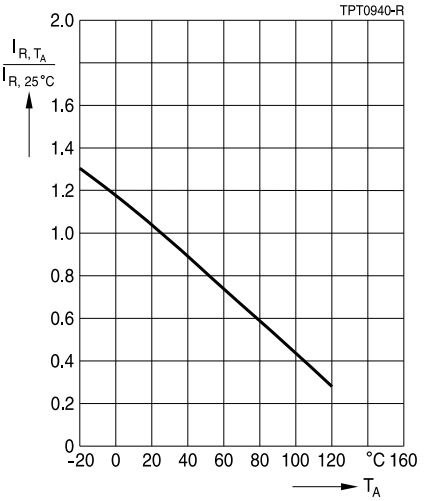
PTC current I_{PTC} versus PTC voltage V_{PTC} (measured at 25 °C in still air)



Switching time t_S versus switching current I_S (measured at 25 °C in still air)



Rated current I_R versus ambient temperature T_A (measured in still air)



Cautions and warnings

General

- EPCOS thermistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- Ensure suitability of thermistor through reliability testing during the design-in phase. The thermistors should be evaluated taking into consideration worst-case conditions.

Storage

- Store thermistors only in original packaging. Do not open the package before storage.
- Storage conditions in original packaging: storage temperature −25 °C ... +45 °C, relative humidity ≤75% annual mean, maximum 95%, dew precipitation is inadmissible.
- Avoid contamination of thermistors surface during storage, handling and processing.
- Avoid storage of thermistor in harmful environment with effect on function on long-term operation (examples given under operation precautions).
- Use thermistor within the following period after delivery:
  - Through-hole devices (housed and leaded PTCs): 24 months
  - Motor protection sensors, glass-encapsulated sensors and probe assemblies: 24 months
  - Telecom pair and quattro protectors (TPP, TQP): 24 months
  - Leadless PTC thermistors for pressure contacting: 12 months
  - Leadless PTC thermistors for soldering: 6 months
  - SMDs in EIA sizes 3225 and 4032, and for PTCs with metal tags: 24 months
  - SMDs in EIA sizes 0402, 0603, 0805 and 1210: 12 months

Handling

- PTCs must not be dropped. Chip-offs must not be caused during handling of PTCs.
- Components must not be touched with bare hands. Gloves are recommended.
- Avoid contamination of thermistor surface during handling.

Soldering (where applicable)

- Use rosin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended.
- Complete removal of flux is recommended.
- Standard PTC heaters are not suitable for soldering.

Mounting

- Electrode must not be scratched before/during/after the mounting process.
- Contacts and housing used for assembly with thermistor have to be clean before mounting. Especially grease or oil must be removed.
- When PTC thermistors are encapsulated with sealing material, the precautions given in chapter "Mounting instructions", "Sealing and potting" must be observed.
- When the thermistor is mounted, there must not be any foreign body between the electrode of the thermistor and the clamping contact.
- The minimum force of the clamping contacts pressing against the PTC must be 10 N.
- During operation, the thermistor's surface temperature can be very high. Ensure that adjacent components are placed at a sufficient distance from the thermistor to allow for proper cooling at the thermistors.
- Ensure that adjacent materials are designed for operation at temperatures comparable to the surface temperature of thermistor. Be sure that surrounding parts and materials can withstand this temperature.
- Avoid contamination of thermistor surface during processing.

Operation

- Use thermistors only within the specified temperature operating range.
- Use thermistors only within the specified voltage and current ranges.
- Environmental conditions must not harm the thermistors. Use thermistors only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.
- Be sure to provide an appropriate fail-safe function to prevent secondary product damage caused by abnormal function (e.g. use VDR for limitation of overvoltage condition).

Symbols and terms

A	Area
C	Capacitance
C_{th}	Heat capacity
f	Frequency
I	Current
I_{max}	Maximum current
I_R	Rated current
I_{res}	Residual current
I_{PTC}	PTC current
I_r	Residual currrent
$I_{r,oil}$	Residual currrent in oil (for level sensors)
$I_{r,air}$	Residual currrent in air (for level sensors)
I_{RMS}	Root-mean-square value of current
I_S	Switching current
I_{Smax}	Maximum switching current
LCT	Lower category temperature
N	Number (integer)
N_c	Operating cycles at V_{max}, charging of capacitor
N_f	Switching cycles at V_{max}, failure mode
P	Power
P_{25}	Maximum power at 25 °C
P_{el}	Electrical power
P_{diss}	Dissipation power
R_G	Generator internal resistance
R_{min}	Minimum resistance
R_R	Rated resistance
ΔR_R	Tolerance of R_R
R_P	Parallel resistance
R_{PTC}	PTC resistance
R_{ref}	Reference resistance
R_S	Series resistance
R_{25}	Resistance at 25 °C
$R_{25,match}$	Resistance matching per reel/ packing unit at 25 °C
ΔR_{25}	Tolerance of R_{25}
T	Temperature
t	Time
T_A	Ambient temperature
t_a	Thermal threshold time

T_C	Ferroelectric Curie temperature
t_E	Settling time (for level sensors)
T_R	Rated temperature
T_{sense}	Sensing temperature
T_{op}	Operating temperature
T_{PTC}	PTC temperature
t_R	Response time
T_{ref}	Reference temperature
T_{Rmin}	Temperature at minimum resistance
t_S	Switching time
T_{surf}	Surface temperature
UCT	Upper category temperature
V or V_{el}	Voltage (with subscript only for distinction from volume)
$V_{c(max)}$	Maximum DC charge voltage of the surge generator
$V_{F,max}$	Maximum voltage applied at fault conditions in protection mode
V_{RMS}	Root-mean-square value of voltage
V_{BD}	Breakdown voltage
V_{ins}	Insulation test voltage
$V_{link,max}$	Maximum link voltage
V_{max}	Maximum operating voltage
$V_{max,dyn}$	Maximum dynamic (short-time) operating voltage
V_{meas}	Measuring voltage
$V_{meas,max}$	Maximum measuring voltage
V_R	Rated voltage
V_{PTC}	Voltage drop across a PTC thermistor
α	Temperature coefficient
Δ	Tolerance, change
δ_{th}	Dissipation factor
τ_{th}	Thermal cooling time constant
λ	Failure rate
$\boxed{e}$	Lead spacing (in mm)

Abbreviations / Notes

SMD Surface-mount devices

* To be replaced by a number in ordering codes, type designations etc.

+ To be replaced by a letter

All dimensions are given in mm.

The commas used in numerical values denote decimal points.

Important notes

The following applies to all products named in this publication:

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2. We also point out that **in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or lifesaving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
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