



#### 30V N-CHANNEL ENHANCEMENT MODE MOSFET 2.5V GATE DRIVE

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C	
30V	$0.025\Omega@V_{GS} = 4.5V$	8.9A	

### **Description**

This new generation of Trench MOSFETs from Diodes Incorporated utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.

# **Applications**

- DC-DC Converters
- Power Management Functions
- Disconnect Switches
- Motor Control

#### **Features**

- Low On-Resistance
- · Fast Switching Speed
- Low Threshold
- Low Gate Drive
- Low Profile SO-8 Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

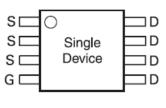
#### **Mechanical Data**

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound,
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 <sup>(3)</sup>
- Weight: 0.076 grams (Approximate)

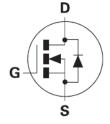
SO-8



Top View



Top View Pin Out Configuration



Equivalent Circuit

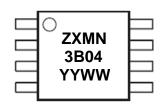
# **Ordering Information** (Note 4)

Part Number	Case	Reel Size	Tape Width	Quantity Per Reel
ZXMN3B04N8TA	SO-8	7"	12mm	500 Units
ZXMN3B04N8TC	SO-8	13"	12mm	2500 Units

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**



ZXMN3B04 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 17 = 2017) WW = Week Code (01 to 53)



# **Maximum Ratings**

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V <sub>DSS</sub>	30	V
Gate-Source Voltage		V <sub>GS</sub>	±12	V
Continuous Drain Current @V <sub>GS</sub> = 4.5V	$T_A = +25^{\circ}\text{C (Note 6)}$ $T_A = +70^{\circ}\text{C (Note 6)}$ $T_{A=} +25^{\circ}\text{C (Note 5)}$	I <sub>D</sub>	8.9 7.3 7.2	Α
Pulsed Drain Current (Note 7)		I <sub>DM</sub>	45	Α
Continuous Source Current (Body Diode) (Note 6)		I <sub>S</sub>	4.5	Α
Pulsed Source Current (Body Diode) (Note 7)		I <sub>SM</sub>	45	Α
Power Dissipation at T <sub>A</sub> = +25°C (Note 5) Linear Derating Factor		P <sub>D</sub>	2 16	W mW/°C
Power Dissipation at T <sub>A</sub> = +25°C (Note 6) Linear Derating Factor		P <sub>D</sub>	3 24	W mW/°C
Operating and Storage Temperature Range		$T_{J}, T_{STG}$	-55 to +150	ů

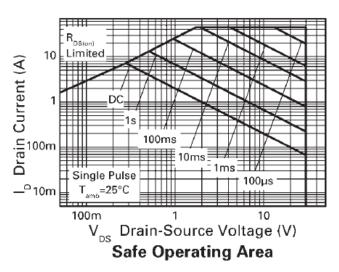
### **Thermal Characteristics**

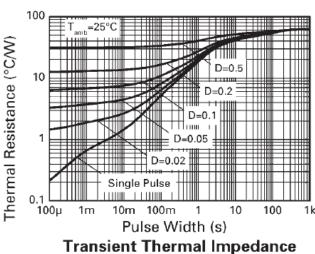
Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	62.5	°C // /
Thermal Resistance, Junction to Ambient (Note 6)	$R_{ heta JA}$	41.4	°C/W

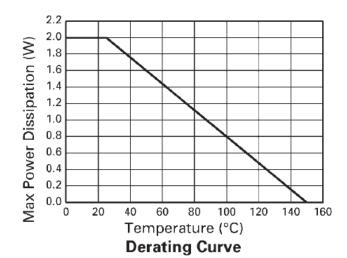
Notes: 5. For a device surface mounted on 50mm x 50mm FR-4 PCB with high coverage of single sided 2oz copper, in still air conditions.

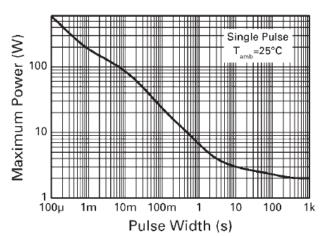
6. For a device surface mounted on FR-4 PCB measured at t ≤10 sec.

<sup>7.</sup> Repetitive rating - 25mm x 25mm FR-4 PCB, D=0.02, pulse width 300µs - pulse width limited by maximum junction temperature.









**Pulse Power Dissipation** 



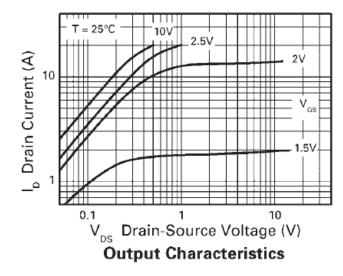
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

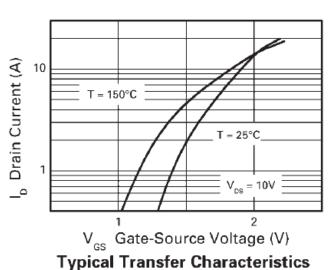
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
STATIC							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	0.5	μΑ	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Body Leakage	I <sub>GSS</sub>	_	_	100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
Gate-Source Threshold Voltage	V <sub>GS(TH)</sub>	0.7	_	_	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance (Note 8)	D	_	0.021	0.025	Ω	$V_{GS} = 4.5V, I_D = 7.2A$	
Static Dialii-Source Off-Resistance (Note 6)	R <sub>DS(ON)</sub>	_	0.028	0.040		$V_{GS} = 2.5V, I_D = 5.7A$	
Forward Transconductance (Notes 8 and 10)	<b>g</b> fs	_	24	_	S	$V_{DS} = 15V, I_D = 7.2A$	
DYNAMIC (Note 10)							
Input Capacitance	Ciss	_	2480	_		V 45V 6 4 0MH-	
Output Capacitance	Coss	_	318	_	pF	$V_{DS} = 15V, f = 1.0MHz,$ $V_{GS} = 0V$	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	184	_		VGS = UV	
SWITCHING (Notes 9 and 10)							
Turn-On Delay Time	t <sub>D(ON)</sub>	_	9	_		$V_{DD} = 15V, R_G = 6.0\Omega, I_D = 1A, V_{GS} = 4.5V$	
Rise Time	t <sub>R</sub>	_	11.5	_	ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	40	_	113		
Fall Time	t <sub>F</sub>	_	16.6	_		!	
Total Gate Charge	Qg	_	23.1	_		$V_{DS} = 15V$ , $V_{GS} = 4.5V$ , $I_{D} = 7.2A$	
Gate-Source Charge	Q <sub>gs</sub>	_	4.9	_	nC		
Gate-Drain Charge	$Q_{gd}$	_	6.2	_			
SOURCE-DRAIN DIODE							
Diode Forward Voltage (Note 8)	$V_{SD}$	_	0.85	0.95	V	$T_J = +25$ °C, $I_S = 8A$ , $V_{GS} = 0V$	
Reverse Recovery Time (Note 10)	t <sub>RR</sub>		17.9	_	ns	di/dt = 100A/μs, I <sub>F</sub> = 3.2A,	
Reverse Recovery Charge (Note 10)	$Q_{RR}$		10		nC	T <sub>J</sub> = +25°C	

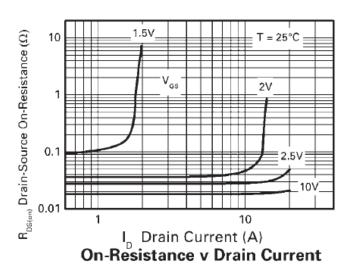
Notes:

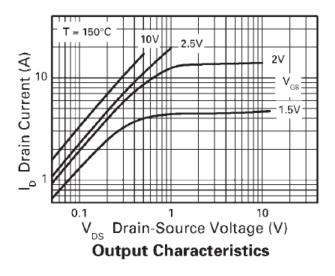
<sup>8.</sup> Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%.
9. Switching characteristics are independent of operating junction temperature.
10. For design aid only, not subject to production testing.

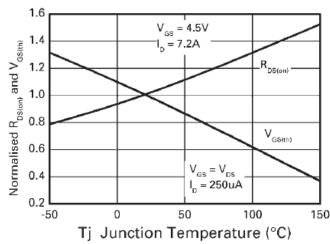




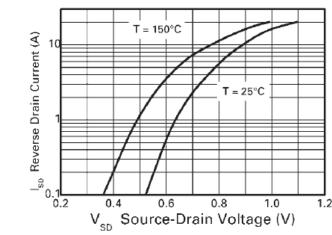






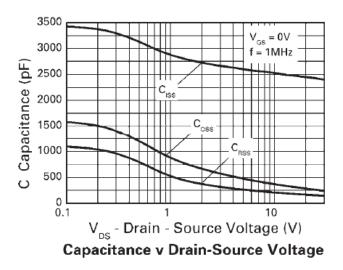


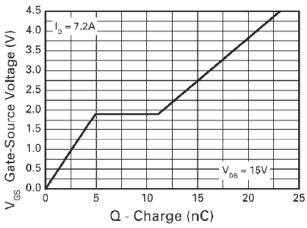
Normalised Curves v Temperature



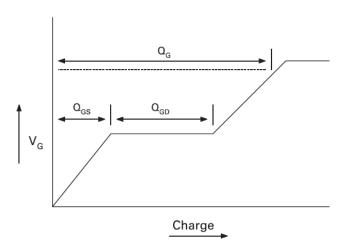
Source-Drain Diode Forward Voltage



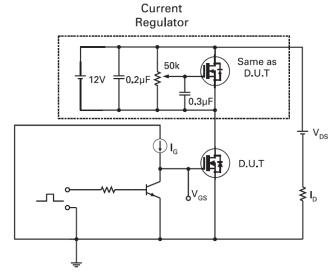




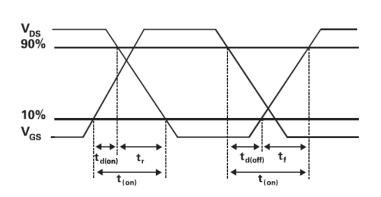
Gate-Source Voltage v Gate Charge



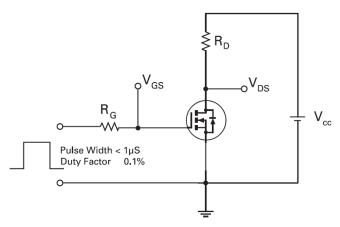
**Basic Gate Charge Waveform** 



**Gate Charge Test Circuit** 



**Switching Time Waveforms** 



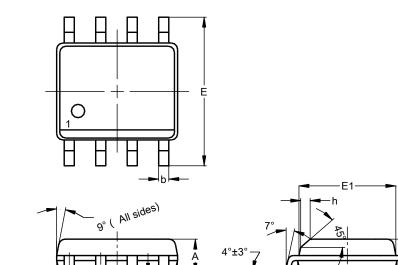
**Switching Time Test Circuit** 



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

**SO-8** 



SO-8					
Dim	Min	Max	Тур		
Α	1.40	1.50	1.45		
A1	0.10	0.20	0.15		
b	0.30	0.50	0.40		
O	0.15	0.25	0.20		
D	4.85	4.95	4.90		
Е	5.90	6.10	6.00		
E1	3.80	3.90	3.85		
E0	3.85	3.95	3.90		
е	-		1.27		
h	ı		0.35		
L	0.62	0.82	0.72		
Q	0.60	0.70	0.65		
All Dimensions in mm					

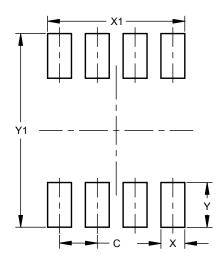
# **Suggested Pad Layout**

RO.1.

Please see http://www.diodes.com/package-outlines.html for the latest version.

**SO-8** 

E0



Dimensions	Value (in mm)
С	1.27
X	0.802
X1	4.612
Y	1.505
V1	6.50

Gauge Plane Seating Plane



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