

NOT RECOMMENDED FOR NEW DESIGN USE DMP3125L



DMG2307L

P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
-30V	90mΩ @ V _{GS} = -10V	-3.8A
	134mΩ @ V _{GS} = -4.5V	-3.1A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- Load Switch for Portable Devices

Features and Benefits

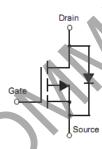
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

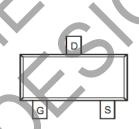
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 20863
- Weight: 0.08 grams (Approximate)







Internal Schematic



Top View

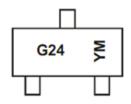
Ordering Information (Note 4)

	_	_		
Part Number			Case	Packaging
DMG2307L-7		<i>p</i>	SOT23	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



G24 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Year	2018	2019	2020	2021	202	2 20	23	2024	2025	2026	2027	2028
Code	F	G	Н	1	J		<	L	М	N	0	Р
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code						_	-				N.I.	7



NOT RECOMMENDED FOR NEW DESIGN USE DMP3125L

DMG2307L

Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	-30	V	
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = -10V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	-2.5 -2.0	Α
Continuous Drain Current (Note 6) V _{GS} = -10V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	-3.8 -3.0	Α
Continuous Drain Current (Note 6) V _{GS} = -10V	t ≦10sec	$T_A = +25$ °C $T_A = +70$ °C	I _D	-4.6 -3.6	Α
Continuous Drain Current (Note 6) V _{GS} = -4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	-3.1 -2.5	Α
Pulsed Drain Current (Note 6)			I _{DM}	-20	Α

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	Po	0.76	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA}	159	°C/W
Total Power Dissipation (Note 6)	PD	1.36	W
Thermal Resistance, Junction to Ambient (Note 6)	Reja	94	°C/W
Total Power Dissipation (Note 6) t ≤ 10sec	PD	1.9	W
Thermal Resistance, Junction to Ambient (Note 6) t ≤ 10sec	Reja	65.8	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

			_			
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)		1 3				
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current @T _C = +25°C	C I _{DSS}	-	_	-1.0	μΑ	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-1.0	_	-3.0	٧	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
Static Drain-Source On-Resistance	Program	_	70	90	mΩ	$V_{GS} = -10V, I_D = -2.5A$
Static Drain-Source On-Nesistance	R _{DS(ON)}	_	105	134	11122	$V_{GS} = -4.5V$, $I_D = -2.5A$
Forward Transfer Admittance	Y _{fs}	_	4.8	_	S	$V_{DS} = -10V$, $I_{D} = -2.5A$
Diode Forward Voltage (Note 6)	V_{SD}	_	-0.75	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	371.3	_	pF	151/11/ 01/
Output Capacitance	Coss	_	51.3	_	pF	$V_{DS} = -15V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	45.9		рF	I = I:OWII IZ
Gate Resistance	R_{g}	_	17		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	4.0		nC	
Total Gate Charge (V _{GS} = -10V)	Q_{g}	_	8.2		nC	$V_{GS} = -10V, V_{DS} = -15V,$
Gate-Source Charge	Q _{gs}	_	0.9	_	nC	$I_D = -3A$
Gate-Drain Charge	Q_{gd}	_	1.2	_	nC	
Turn-On Delay Time	t _{D(ON)}	_	4.8	_	ns	1/ 45)/)/ 40)/
Turn-On Rise Time	t _R	_	7.3		ns	$V_{DS} = -15V, V_{GS} = -10V,$
Turn-Off Delay Time	t _{D(OFF)}	_	22.4	_	ns	$R_L = 15\Omega$, $R_G = 6\Omega$, $I_D = -1A$
Turn-Off Fall Time	t _F	_	13.4	_	ns	1D = -1A

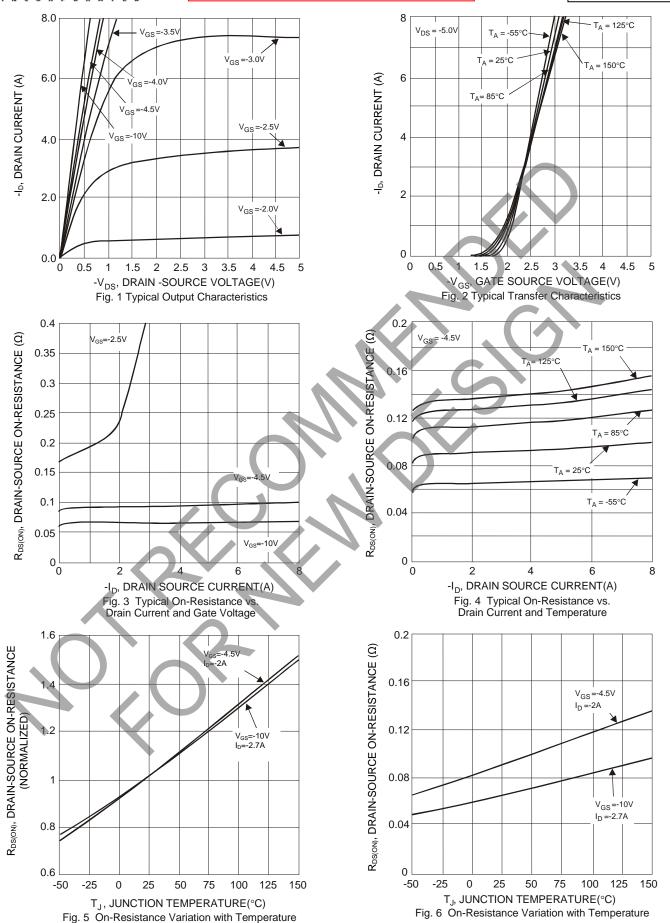
Notes:

- Device mounted on FR-4 PCB, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.



NOT RECOMMENDED FOR NEW DESIGN USE DMP3125L

DMG2307L





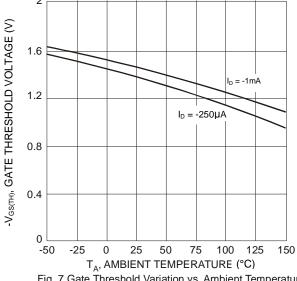
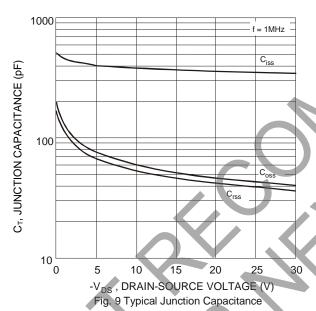
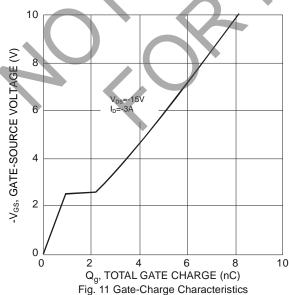
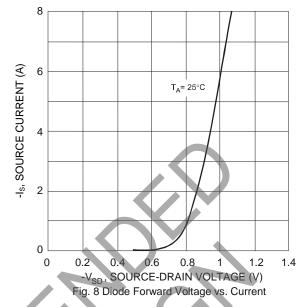


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







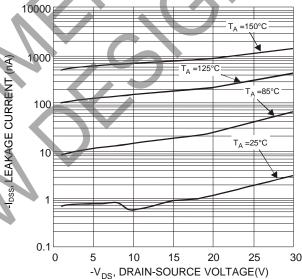


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage

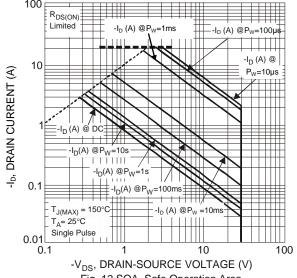
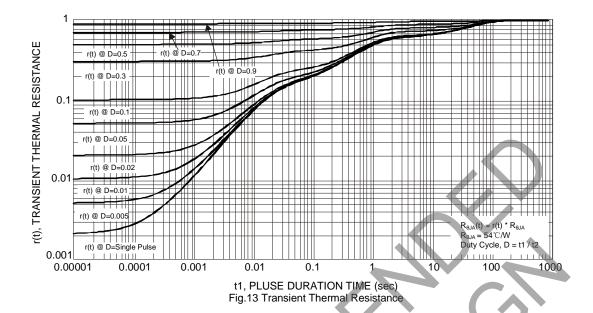


Fig. 12 SOA, Safe Operation Area



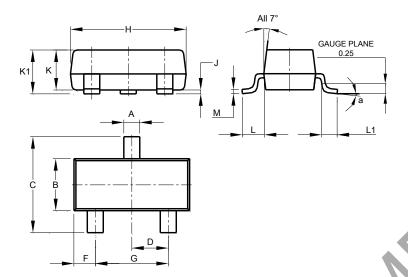




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

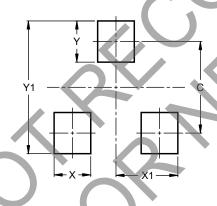


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
H	2.80	3.00	2.90				
7	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
M	0.085	0.150	0.110				
а	0°	8°					
All	Dimens	ions in	mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	8.0
X1	1.35
Y	0.9
Y1	29



NOT RECOMMENDED FOR NEW DESIGN USE DMP3125L

DMG2307L

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2018, Diodes Incorporated

www.diodes.com