

## **Cypress Roadmap: Wireless Solutions for** The IoT

**IoT = Internet of Things** 





**Owner: SGUP** BUM: MIHO

Cypress Roadmap: Wireless Solutions for The IoT

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Page 2

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Project 'Detector' C TopDesign.cvp Detector.cydwr

Header Files A device. B O Source Files

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Voltage DAC (8-bit

mm

## Wireless Portfolio



	Bluetooth	Wi-Fi	Wi-Fi + Bluetooth Combo	Automotive
			IEEE 802.11a/b/g/n/ac WLAN <sup>1</sup> + Bluetooth Up to 867 Mbps Wi-Fi, 1-3 Mbps Bluetooth Dual Band (2.4/5 GHz), 2x2 MIMO <sup>2</sup>	IEEE 802.11a/b/g/n/ac WLAN + Bluetooth Up to 867 Mbps, Dual Band (2.4/5 GHz), 2x2 MIMO, 1+1RSDB <sup>3</sup>
nce		IEEE 802.11a/b/g/n WLAN + MCU Up to 150 Mbps ARM <sup>®</sup> Cortex <sup>®</sup> -R4/-M3 MCU	IEEE 802.11a/b/g/n WLAN + Bluetooth Up to 300 Mbps Wi-Fi, 1-3 Mbps Bluetooth Dual Band (2.4/5 GHz), 2x2 MIMO	IEEE 802.11a/b/g/n/ac WLAN + Bluetooth Up to 433 Mbps PHY rate Dual Band (2.4/5 GHz), 1x1
rmai				
Integration and Performance	Bluetooth (BR⁴ + EDR⁵ + BLE⁵) + MCU 1-3 Mbps, Class 1/2/3 <sup>7</sup> , ARM Cortex <sup>®</sup> -M4/-M3 MCU	IEEE 802.11a/b/g/n WLAN Up to 150 Mbps 2x2 MIMO		Bluetooth (BR + EDR) 1-3 Mbps Class 1/2/3
	PSoC BLE 1-2 Mbps, CapSense <sup>®</sup> , AFE <sup>8</sup> , Opamp, TIA <sup>9</sup> ARM Cortex <sup>®</sup> -M0/-M4/-M4 and M0+ MCU			
Inte	PRoC BLE 1 Mbps, CapSense® ARM Cortex <sup>®</sup> -M0 MCU			
	Bluetooth Low Energy (BLE) + MCU 1-2 Mbps, ARM Cortex®-M3 MCU			
<sup>2</sup> Mult	eless Local Area Network <sup>4</sup> Basic Rate iple-input multiple-output <sup>5</sup> Enhanced I d simultaneous dual band <sup>6</sup> Bluetooth L	Data Rate <sup>8</sup> Analog front end	3 (1 m) Status	Concept Development Sampling Production
001-89 Rev *P		Cypress Roadmap: Wireles	ss Solutions for The IoT	2



### Wireless Solutions for The Internet of Things (IoT) Roadmap BLE PORTFOLIO

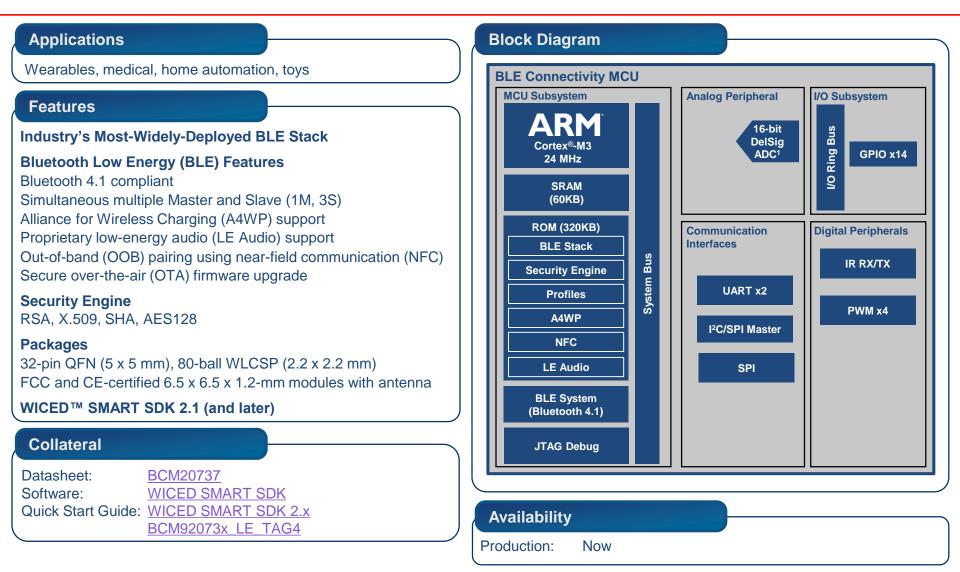
## Bluetooth Low Energy (BLE) Portfolio



WICED™		PSoC <sup>®</sup> Creator™			
BLE + MCU		PRoC™ BLE (MCU + Touch¹)		PSoC 4 BLE (MCU + Touch + Mixed-Signal)	
IR KB Cry 4 5	BCM20719 Q416 2M4 <sup>2</sup> , SPI, UART, I <sup>2</sup> C <sup>3</sup> , TX/RX <sup>4</sup> , ADC, 6 PWM, 3 Scanner <sup>5</sup> , Mouse QD <sup>6</sup> , ypto <sup>7</sup> , 4 TRIAC Control, 40 GPIO, 1MB Flash, 512KB RAM, BT <sup>6</sup> 4.2, ops support, WICED SDK <sup>9</sup>				
		CYBL1117x CM0, DMA, 2 SCB, I <sup>2</sup> S 4 TCPWM, 4 PWM, ADC, 36 GPIO, 256KB Flash, 32KB RAM, BT 4.2, PSoC Creator	CYBL1147x/57x CM0, DMA, 2 SCB, I <sup>2</sup> S 2-Finger <sup>1</sup> , 4 TCPWM, 4 PWM, ADC, 36 GPIO, 256KB Flash, 32KB RAM, BT 4.2, PSoC Creator	CY8C41x8-BL5xx CM0, DMA, 2 SCB, 4 Opamp, 2 CMP, ADC, 4 TCPWM, 36 GPIO, 256KB Flash, 32KB RAM, BT 4.2, PSoC Creator	CY8C42x8-BL5xx CM0, DMA, 2 SCB, 4 Opamp, 2 CMP, 4 UDB, ADC, 4 TCPWM, 36 GPIO, 256KB Flash, 32KB RAM, BT 4.2, PSoC Creator
IR LE 1	BCM20737 CM3, SPI, UART, I <sup>2</sup> C, R TX/RX, ADC, 4 PWM, E Audio, NFC <sup>15</sup> , Crypto, 14 GPIO, 60KB RAM, BT 4.1, WICED SDK		<b>CYBL1057x</b> CM0, 2 SCB, I <sup>2</sup> S 2-Finger, 4 TCPWM, 4 PWM, ADC, 36 GPIO, 256KB Flash, 32KB RAM, BT 4.1, PSoC Creator	CY8C41x8-BL4xx CM0, 2 SCB, ADC, 4 Opamp, 2 CMP, 4 TCPWM, 36 GPIO, 256KB Flash, 32KB RAM, BT 4.1, PSoC Creator	CY8C42x8-BL4xx CM0, 2 SCB, ADC, 4 Opamp, 2 CMP, 4 UDB, 4 TCPWM, 36 GPIO, 256KB Flash, 32KB RAM, BT 4.1, PSoC Creator
IR 4 PW	<b>BCM20736</b> CM3, SPI, UART, I <sup>2</sup> C, TX/RX, ADC, A4WP <sup>16</sup> , /M, 40 GPIO, 60KB RAM, BT 4.1, WICED SDK	<b>CYBL1016x</b> CM0, 2 SCB, I <sup>2</sup> S, 4 TCPWM, 4 PWM, ADC, 36 GPIO, 128KB Flash, 16KB RAM, BT 4.1, PSoC Creator	CYBL1046x/57x CM0, 2 SCB, I <sup>2</sup> S, 2-Finger, 4 TCPWM, 4 PWM, ADC, 36 GPIO, 256KB Flash, 32KB RAM, BT 4.1, PSoC Creator	<b>CY8C41x7-BL4xx</b> CM0, 2 SCB, ADC, 4 Opamp, 2 CMP, 4 TCPWM, 36 GPIO, 128KB Flash, 16KB RAM, BT 4.1, PSoC Creator	CY8C42x7-BL4xx CM0, 2 SCB, ADC, 4 Opamp, 2 CMP, 4 UDB, 4 TCPWM, 36 GPIO, 128KB Flash, 16KB RAM, BT 4.1, PSoC Creator
Integration and Flexibility					
1 Touch-sensing technology with up to 2-finger gestures       7       Cryptographic accelerator block for 8         2 ARM® Cortex®-M0/M0+/M3/M4       8       Bluetooth Specification         3 Broadcom serial communications block       9       Software development kit         4 Infrared transmit and receive       10       Direct memory access         5 Keyboard scanner       11       Serial communication block         6 Mouse quadrature decoder       12       Comparator		elopment kit y access nication block	<ul> <li><sup>13</sup> Universal digital block</li> <li><sup>14</sup> Timer/Counter/PWM</li> <li><sup>15</sup> Out-of-Band pairing with NFC</li> <li><sup>16</sup> Alliance for Wireless Power E Profile</li> </ul>		opment Sampling Production

### Bluetooth Low Energy Connectivity MCU with Security and Wireless Charging

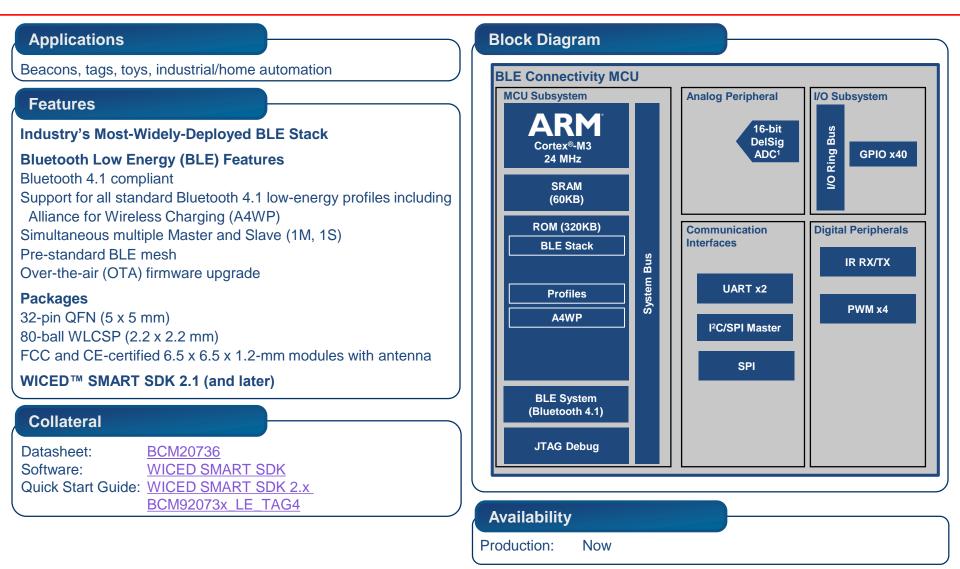




#### <sup>1</sup> Effective number of bits is 10 at 187 ksps

### Bluetooth Low Energy Connectivity MCU with Wireless Charging

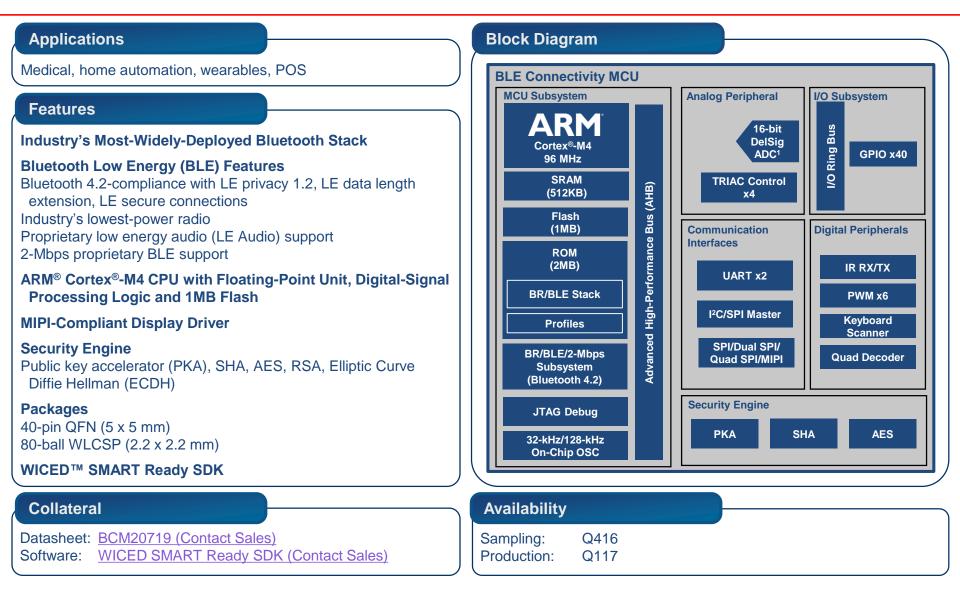




#### <sup>1</sup> Effective number of bits is 10 at 187 ksps

### Ultra Low Power Bluetooth Smart Ready Connectivity Secure MCU





#### <sup>1</sup> Effective number of bits is 10 at 187 ksps

001-89683 Owner: SGUP Rev \*P BUM: MIHO

## PSoC<sup>®</sup> 4 BLE (CY8C4xxx-BL)

Programmable System-on-Chip with Bluetooth Low Energy



### **Applications**

Sports and fitness monitors, wearable electronics, medical devices, home automation solutions, game controllers, sensor-based low-power systems for IoT

### Features

**32-bit MCU subsystem** ARM<sup>®</sup> Cortex<sup>®</sup>-M0 with DMA, 256KB flash and 32KB SRAM

### **Programmable Analog Front End**

Four opamps, configurable as PGAs, comparators, filters, etc. One 12-bit, 1-Msps SAR ADC

CapSense<sup>®</sup> with SmartSense<sup>™</sup> Auto-tuning

One Cypress Capacitive Sigma-Delta™ (CSD) controller with touchpad capability

### **Programmable Digital Logic**

Four universal digital blocks (UDBs): custom digital peripherals Four configurable TCPWM<sup>1</sup> blocks: 16-bit timer, counter or PWM Two configurable serial communication blocks (SCBs)<sup>2</sup>

I<sup>2</sup>C master or slave, SPI master or slave, or UART

### Packages

Collateral

56-pin QFN, 68/76-ball CSP and thin CSP<sup>3</sup>

**Bluetooth Smart Connectivity with Bluetooth 4.2** 2.4-GHz BLE radio with integrated balun

#### **PSoC 4 BLE One-Chip Solution** I/O Subsystem **MCU Subsystem Programmable Analog Blocks** GPIO x8 Opamp SAR ADC x4 Cortex<sup>®</sup>-M0 48 MHz CMP Programmable Interconnect and Routing Advanced High-Performance Bus (AHB) CSD **GPIO x8** x2 BLE System **Programmable Digital** (Bluetooth 4.2) **Blocks** Flash **GPIO x8** UDB x4 (256KB) SRAM **TCPWM x4** (32KB) GPIO x8 DMA SCB x2 Segment LCD Drive Serial Wire Debug **GPIO x4** Availability

256KB

Now

Now

128KB

Now

Now

Datasheet:	CY8C4xxx-BL
Software:	PSoC Creator IDE
Application Note:	Getting Started With PSoC 4 BLE

<sup>1</sup> Timer/Counter/Pulse-Width modulator <sup>3</sup> A thinner CSP package, 0.38 mm thick as compared to 0.55 mm for a regular CSP package

<sup>2</sup> Serial communication block configurable as I<sup>2</sup>C/SPI/UART

Sampling:

Production:

**Block Diagram** 

256KB with BLE 4.2

Now

Now

# PROC<sup>TM</sup> BLE (CYBL1x1x/4x/5xx) Programmable Radio-on-Chip with Bluetooth Low Energy



#### **Applications Block Diagram** BLE connectivity, wireless touch mice, wireless keyboards with **PRoC BLE One-Chip Solution** trackpads, wireless trackpads, wireless remote control with **MCU Subsystem** Analog Peripheral I/O Subsystem trackpads, wireless toys **GPIO x8** SAR **Features** ADC CORTEX®-M0 48 MHz 32-bit MCU subsystem ARM® Cortex®-M0 with DMA, 256KB flash and 32KB SRAM Programmable Interconnect and Routing Advanced High-Performance Bus (AHB) CSD GPIO x8 CapSense<sup>®</sup> with SmartSense<sup>™</sup> Auto-tuning BLE System One Cypress Capacitive Sigma-Delta™ (CSD) controller with **Digital Peripherals** (Bluetooth 4.2) touchpad capability **Analog and Digital Peripherals** Flash **GPIO x8** I<sup>2</sup>S (256KB) One 12-bit, 1-Msps SAR ADC Four configurable TCPWM<sup>1</sup> blocks: 16-bit timer, counter or PWM SRAM Two configurable serial communication blocks (SCBs)<sup>2</sup> **TCPWM x4** (32KB) I<sup>2</sup>C master or slave, SPI master or slave, or UART Dedicated I<sup>2</sup>S Tx/Rx interface GPIO x8 DMA SCB x2 Up to four additional PWMs **Packages** Segment LCD Drive 56-pin QFN, 68/76-ball CSP and thin CSP<sup>3</sup> Serial Wire Debug **Bluetooth Smart Connectivity With Bluetooth 4.2 GPIO x4** 2.4-GHz BLE radio with integrated Balun Collateral Availability Datasheet: CYBL1x1x/4x/5xx 256KB with BLE 4.2 128KB 256KB **PSoC Creator IDE** Software: Sampling: Now Now Now Application Note: Getting Started With PRoC BLE Production: Now Now Now <sup>3</sup> A thinner CSP package, 0.38 mm thick as compared to 0.55 mm for a regular CSP package

<sup>1</sup> Timer, Counter, Pulse-Width Modulator

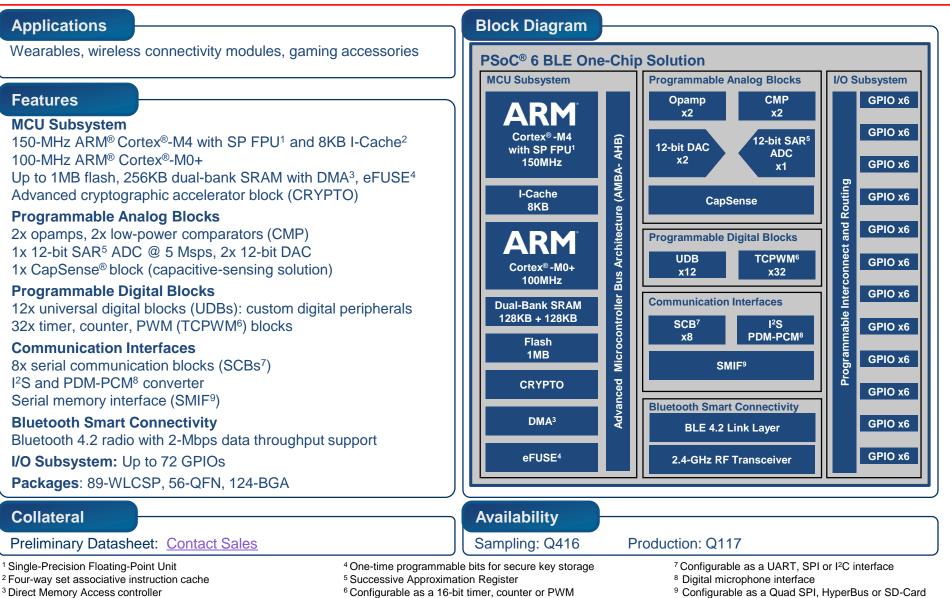
<sup>2</sup> Serial communication block configurable as I<sup>2</sup>C/SPI/UART

001-89683 **Owner: SGUP** Rev \*P BUM: MIHO

Cypress Roadmap: Wireless Solutions for The IoT

### PSoC<sup>®</sup> 6 BLE (NDA) Energy-Efficient One-Chip Wearable Solution





<sup>&</sup>lt;sup>3</sup> Direct Memory Access controller

Cypress Roadmap: Wireless Solutions for The IoT



### Wireless Solutions for The Internet of Things (IoT) Roadmap BLUETOOTH CLASSIC + BLE PORTFOLIO

## Bluetooth Classic + BLE Portfolio

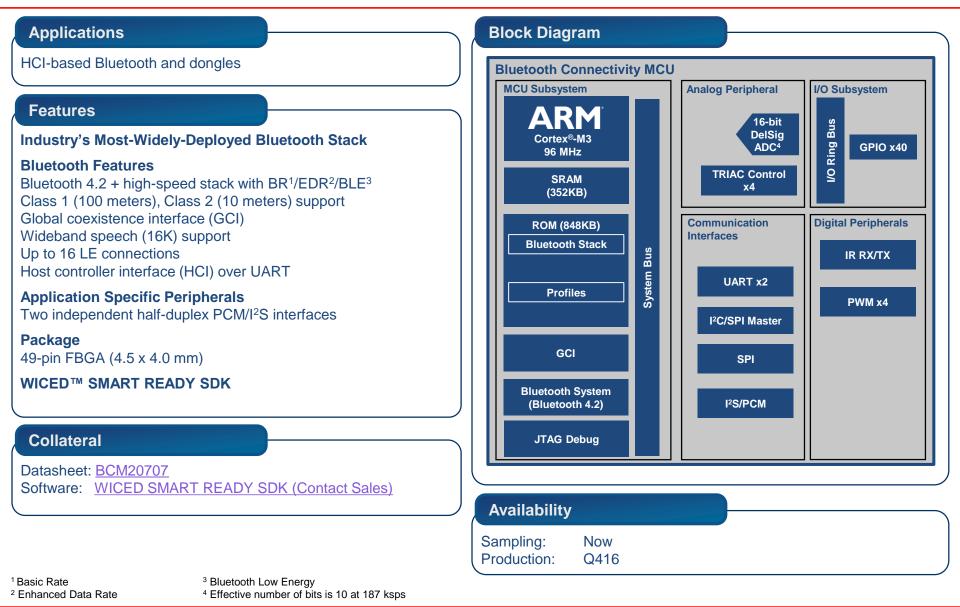


	BR <sup>1</sup> +BLE	BR + EDR <sup>2</sup> + BLE <sup>3</sup>
nce	BCM20735Q41648-MHz CM4 <sup>4</sup> , 2 SPI (Quad/Dual), UART, I <sup>2</sup> C, IR TX/RX <sup>5</sup> , ADC, 6 PWM, KB Scanner <sup>6</sup> , Mouse QD <sup>7</sup> , GCI <sup>8</sup> , 4 TRIAC Control, 40 GPIO, 384KB RAM, BT <sup>9</sup> 4.2 + BLE + 2 Mbps, C1/C2/C3 <sup>10</sup> , WICED SMART READY	BCM207x9 Q416 ULP <sup>11</sup> , 48-MHz CM4, 2 SPI (Quad/Dual), UART, I <sup>2</sup> C, IR TX/RX, ADC, 6 PWM, KB Scanner, Mouse QD, GCI, Crypto <sup>12</sup> , 4 TRIAC Control, 40 GPIO, 1MB Flash, 512KB RAM, BT 4.2 + BLE + 2 Mbps, C2/C3, WICED SMART READY
<b>CPU Performance</b>		Q416 BCM20707 96-MHz CM3, 2 SPI, UART, I <sup>2</sup> C, GCI, 4 PWM, I <sup>2</sup> S/PCM, 24 GPIO, 352KB RAM, BT 4.2 + EDR + BLE, C1/C2 WICED SMART READY
		BCM20704Q416BCM20706Q41696-MHz CM3, UART, I²C, USB 2.0, I²S/PCM, GCI 8 GPIO, 352KB RAM, BT 4.2 + EDR + BLE, C1/C2 HCI-Over-UART/USB 2.096-MHz CM3 Embedded BT SoC, 2 SPI, UART, I²C, IR TX/RX, ADC, 4 PWM, GCI, I²S/PCM, 4 TRIAC Control, 24 GPIO, 352KB RAM, BT 4.2 + EDR + BLE, C1/C2 WICED SMART READY
		Integration



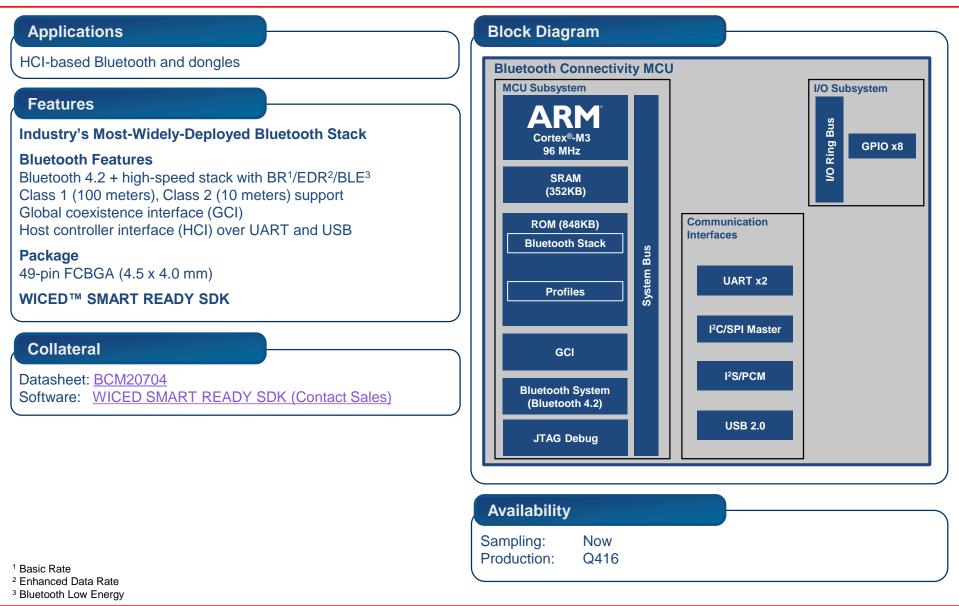
### BCM20707 Bluetooth Connectivity MCU





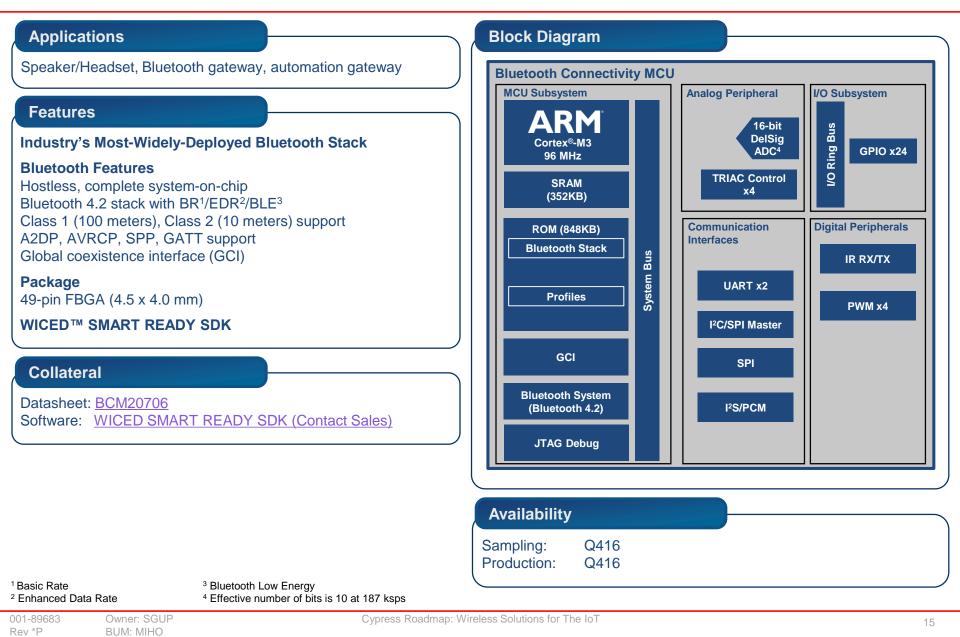
### BCM20704 Bluetooth Connectivity MCU





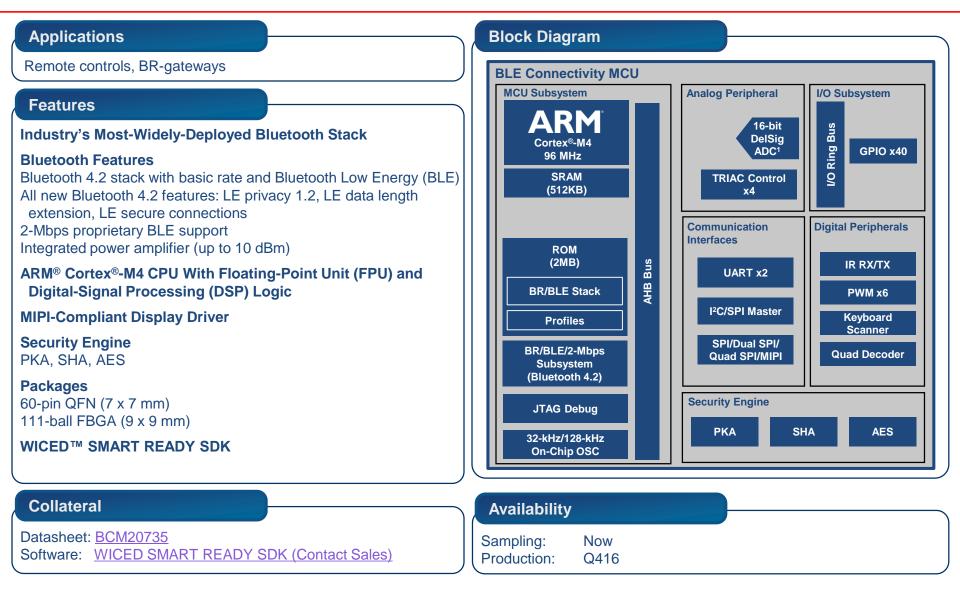
### BCM20706 Bluetooth Connectivity MCU





**Bluetooth Smart and Basic Rate Connectivity MCU** 





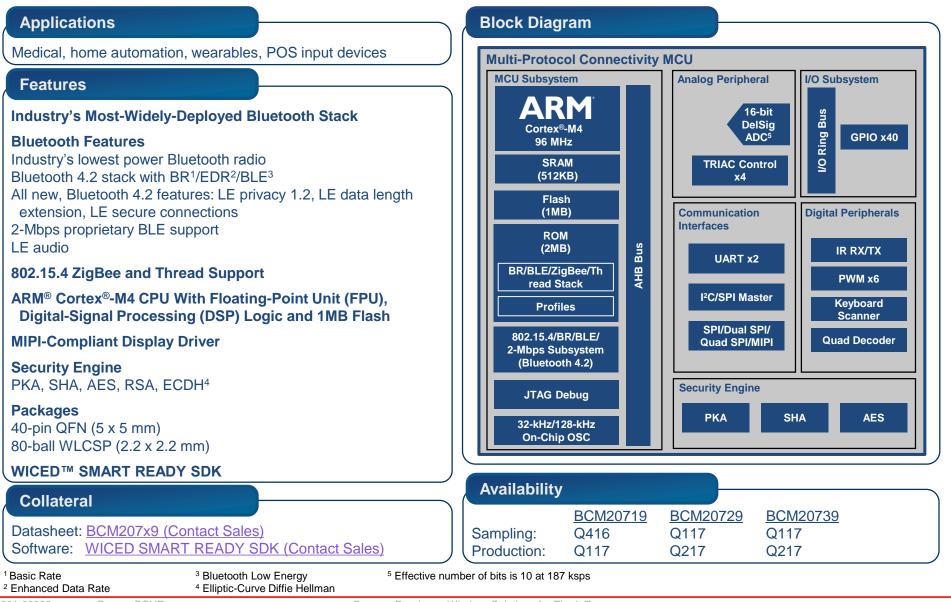
### <sup>1</sup> Effective number of bits is 10 at 187 ksps

001-89683	Owner: SGUF
Rev *P	BUM: MIHO

## BCM207x9

### Ultra Low Power Multi-Protocol Connectivity MCU





Cypress Roadmap: Wireless Solutions for The IoT



### Wireless Solutions for The Internet of Things (IoT) Roadmap Wi-Fi PORTFOLIO

## Wi-Fi Portfolio



Dual Band (2.4/5 GHz)	IEEE 802.11a/b/g/n WLAN <sup>1</sup>		IEEE 802.11a/b/g/n WLAN + MCU BCM43907 Up to 150 Mbps, 802.11a/b/g/n, SISO <sup>2</sup> , 320-MHz CR4 <sup>3</sup> , GCI <sup>4</sup> SECI <sup>5</sup> , Security <sup>6</sup> , CCX <sup>7</sup> , 6 PWMs, Ethernet (RMII/MII), SDIO 3.0 (H/D), USB 2.0 + HSIC (H/D), S/PDIF, 3 UART, Quad SPI, 2 SPI, 2 I <sup>2</sup> S, 2 I <sup>2</sup> C 17 GPIO, Integrated PA <sup>8</sup> for both bands WICED	Î
		BCM43143 Up to 150 Mbps, 802.11b/g/n, SISO GCI SECI, Security, SDIO 2.0, USB 2.0, I <sup>2</sup> C/SPI 19 GPIO, Integrated PA Linux Driver		Data Rate
Single Band (2.4 GHz)		BCM43364 Up to 96 Mbps, 802.11b/g/n, SISO GCI SECI, Security, CCX SDIO 2.0, SPI, 5 GPIO, Integrated PA Linux Driver, WICED	BCM43903 Up to 72 Mbps, 802.11b/g/n, SISO, 160-MHz CR4, GCI SECI, Security, CCX, 6 PWMs, 3 UART, Quad SPI, 2 SPI, 2 I <sup>2</sup> C 17 GPIO, Integrated PA WICED	
Singl (2.4		BCM43362 Up to 72 Mbps, 802.11b/g/n, SISO SECI, Security, SDIO 2.0, SPI, 5 GPIO, Integrated PA Linux Driver, WICED	BCM4390 Up to 72 Mbps, 802.11b/g/n, SISO, 48-MHz CM3, GCI, Security, CCX, 6 PWMs, 4 UART, I <sup>2</sup> S, 2 SPI, I <sup>2</sup> C 24 GPIO, Integrated PA WICED	
<sup>2</sup> Single-in <sup>3</sup> ARM <sup>®</sup> Co	Local Area Ne put single-outp rtex®-M3/R4	put <sup>6</sup> WPA, WAPI STA, WPA2, A <sup>7</sup> Cisco-compatible extension	2, AES, TKIP security features	duction

Cypress Roadmap: Wireless Solutions for The IoT

<sup>8</sup> Power amplifier

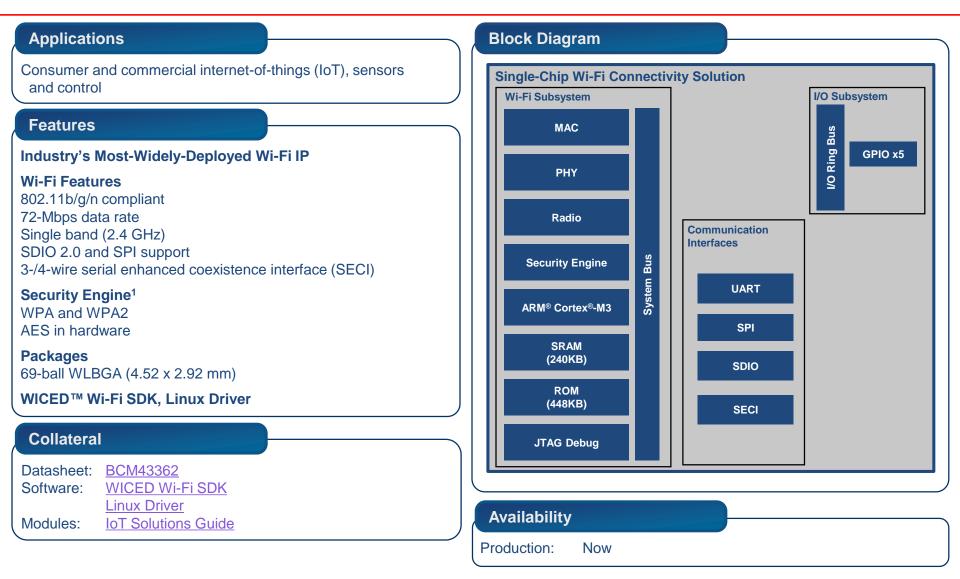
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Availability

### Single-Chip IEEE 802.11n MAC/Baseband/Radio + SDIO Connectivity Solution



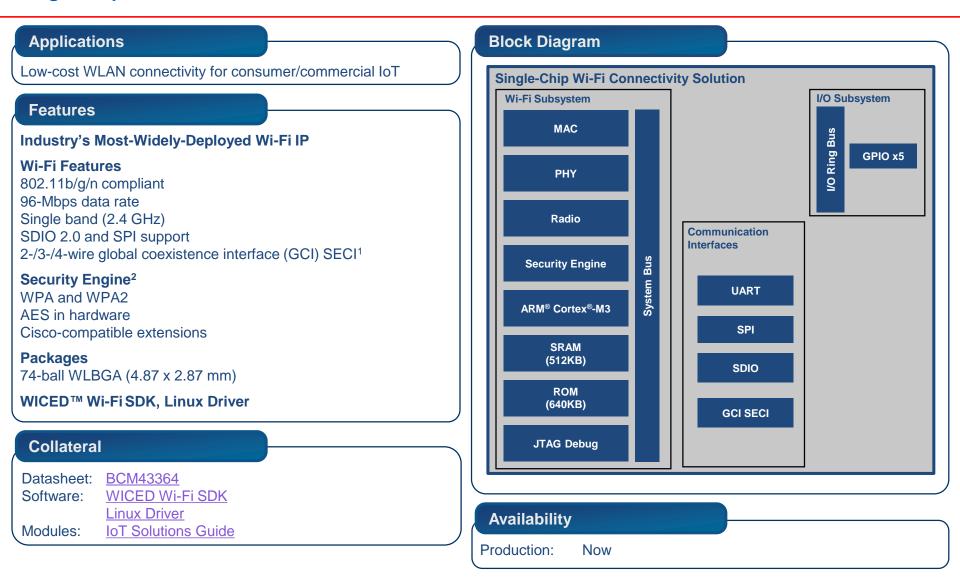


#### <sup>1</sup> WPA, WAPI STA, WPA2, AES, TKIP security features

001-89683	Owner: SGUP
Rev *P	BUM: MIHO

### BCM43364 Single-Chip IEEE 802.11n MAC/Baseband/Radio

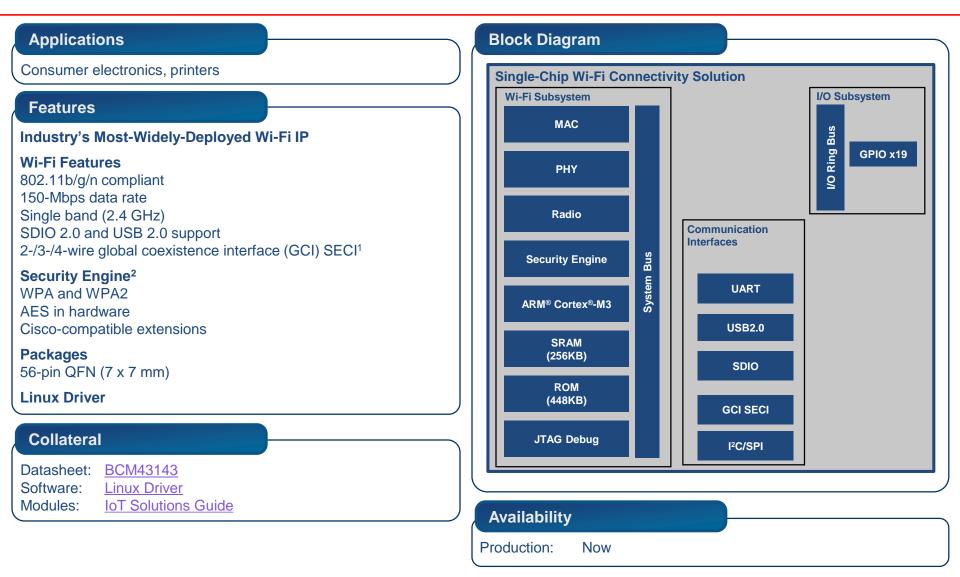




<sup>2</sup> WPA, WAPI STA, WPA2, AES, TKIP security features

### Single-Chip IEEE 802.11n MAC/Baseband/Radio + SDIO/USB Connectivity Solution



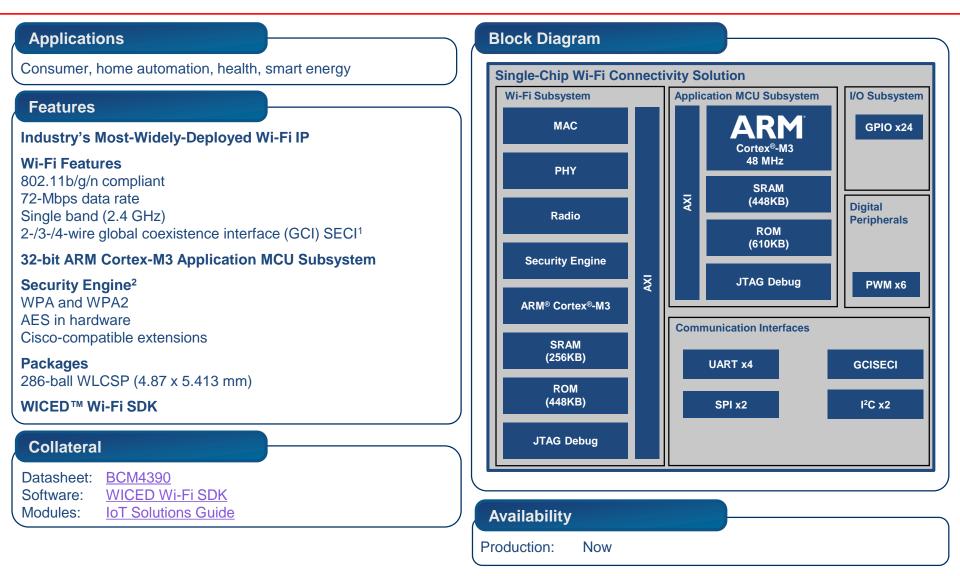


#### <sup>1</sup> Serial-enhanced coexistence interface

<sup>2</sup> WPA, WAPI STA, WPA2, AES, TKIP security features

### IEEE 802.11n System-on-Chip with Embedded Application Processor



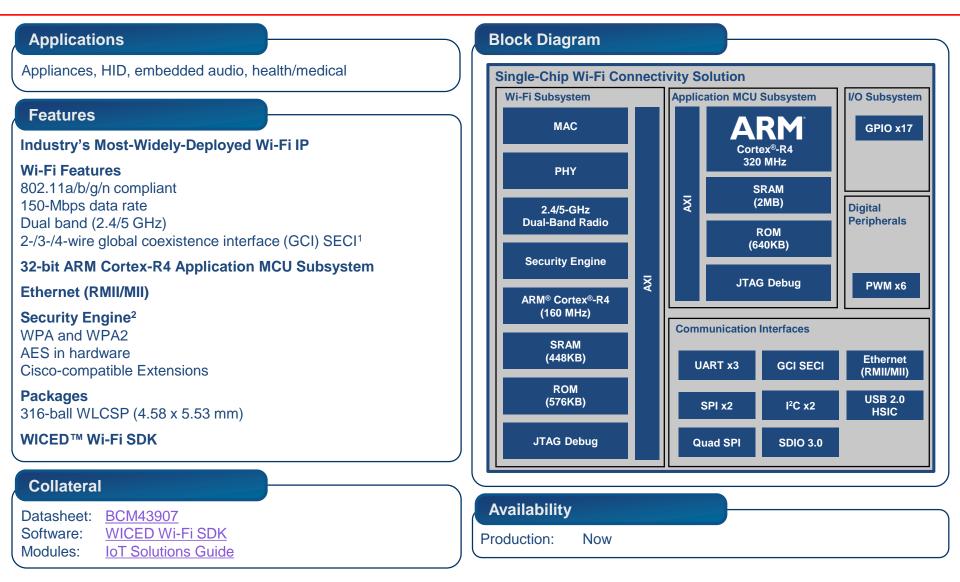


#### <sup>1</sup> Serial-enhanced coexistence interface

<sup>2</sup>WPA, WAPI STA, WPA2, AES, TKIP security features

### IEEE 802.11n System-on-Chip with Embedded Application Processor



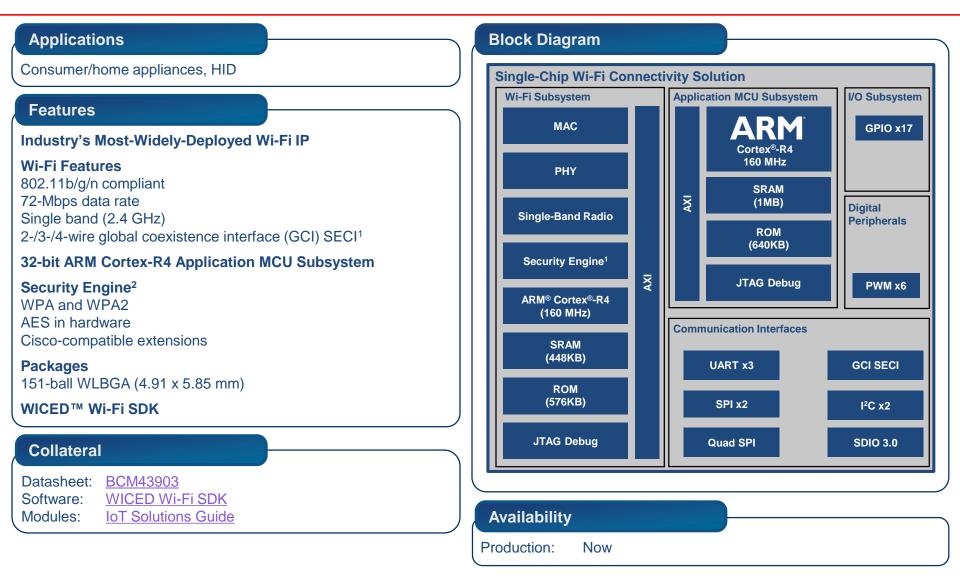


<sup>1</sup> Serial-enhanced coexistence interface

<sup>2</sup> WPA, WAPI STA, WPA2, AES, TKIP security features

### IEEE 802.11n System-on-Chip with Embedded Application Processor





#### <sup>1</sup> Serial-enhanced coexistence interface

<sup>2</sup> WPA, WAPI STA, WPA2, AES, TKIP security features



### Wireless Solutions for The Internet of Things (IoT) Roadmap Wi-Fi + BLUETOOTH COMBO PORTFOLIO

## Wi-Fi + Bluetooth Combo Portfolio

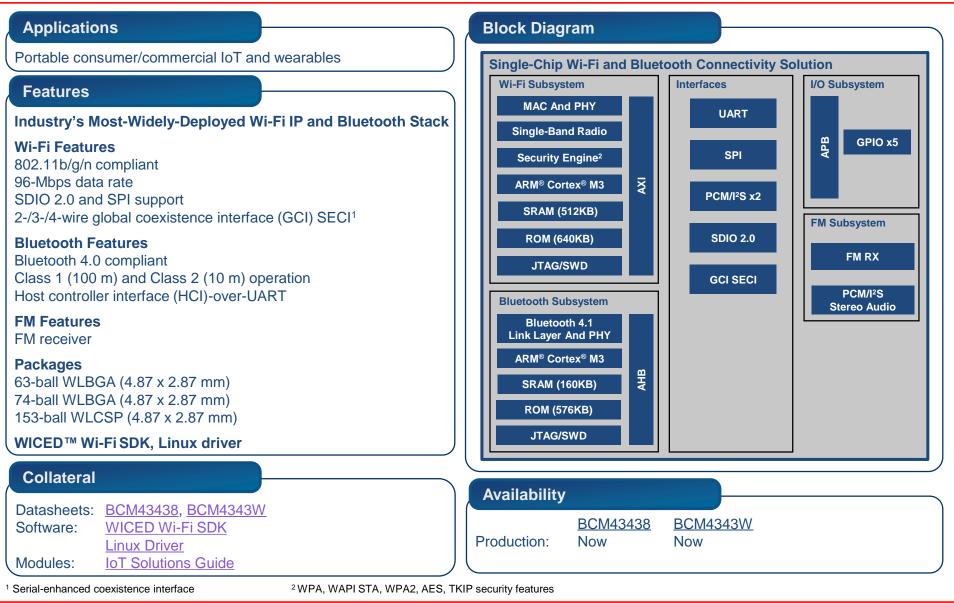


	IEEE 802.11a/b/g/n WLAN <sup>1</sup> + Bluetooth (BR <sup>2</sup> + EDR <sup>3</sup> + BLE <sup>4</sup> )	IEEE 802.11a/b/g/n/ac WLAN + Bluetooth (BR + EDR + BLE)		
Dual-Band (2.4/5 GHz)	<b>BCM43340</b> Up to 150 Mbps, 802.11a/b/g/n, BT <sup>s</sup> 4.0, SECI <sup>6</sup> , Security <sup>7</sup> , SDIO 2.0, SPI, HSIC, HCI-over-UART, 2 PCM/I <sup>2</sup> S, I <sup>2</sup> S/Stereo Audio for FM, 8 GPIOs, Integrated PA <sup>8</sup> , Linux Driver, WICED	BCM43455 Up to 433.3 Mbps, 802.11a/b/g/n/ac, BT4.1, A4WP <sup>9</sup> , GCI <sup>10</sup> SECI, Security, PCIe3.0, SDIO 2.0/3.0, SPI, HCI-over-UART, PCM/I <sup>2</sup> S, FM RX, 15 GPIOs, Integrated PA, Linux Driver BCM4339 Up to 433.3 Mbps, 802.11a/b/g/n/ac, BT4.1, A4WP, GCI SECI, Security, SDIO2.0/3.0, SPI, HCI-over-UART, PCM/I <sup>2</sup> S, FM RX, 16 GPIOs, Integrated PA, Linux Driver		
Single-Band (2.4 GHz)	BCM43438/BCM4343W Up to 96 Mbps, 802.11b/g/n, BT4.1, A4WP, GCI SECI, Security, SDIO2.0, SPI, HCI-over-UART, 2 PCM/I <sup>2</sup> S,FM RX, PCM/Stereo Audio for FM, 5 GPIOs, Integrated PA, Linux Driver, WICED			
	Data Rate			
<sup>2</sup> Basic R <sup>3</sup> Enhanc	<sup>1</sup> Wireless Local Area Network <sup>5</sup> Bluetooth Specification <sup>9</sup> Alliance for Wireless Power BLE Profile       Concept Development Sampling Production <sup>2</sup> Basic Rate <sup>6</sup> Serial-enhanced coexistence interface <sup>10</sup> Global coexistence interface       Status       Image: Concept Development Sampling Production <sup>3</sup> Enhanced Data Rate <sup>7</sup> WPA, WAPI STA, WPA2, AES, TKIP <sup>9</sup> Alliance for Wireless Power BLE Profile       Status       Image: Concept Development Sampling Production <sup>8</sup> Bluetooth Low Energy <sup>8</sup> Power amplifier <sup>9</sup> Alliance for Wireless Power BLE Profile       Status       Image: Concept Development Sampling Production			
001-89683 Rev *P				

## BCM43438/BCM4343W

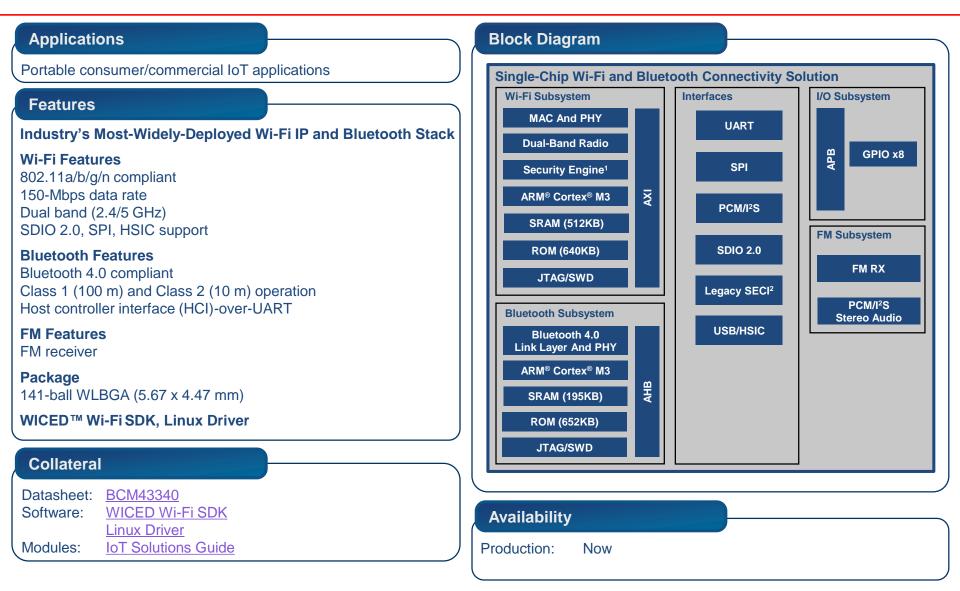
Single chip IEEE 802.11n with Integrated Bluetooth 4.1 and FM Receiver





### Single chip IEEE 802.11n with Integrated Bluetooth 4.0 and FM Receiver

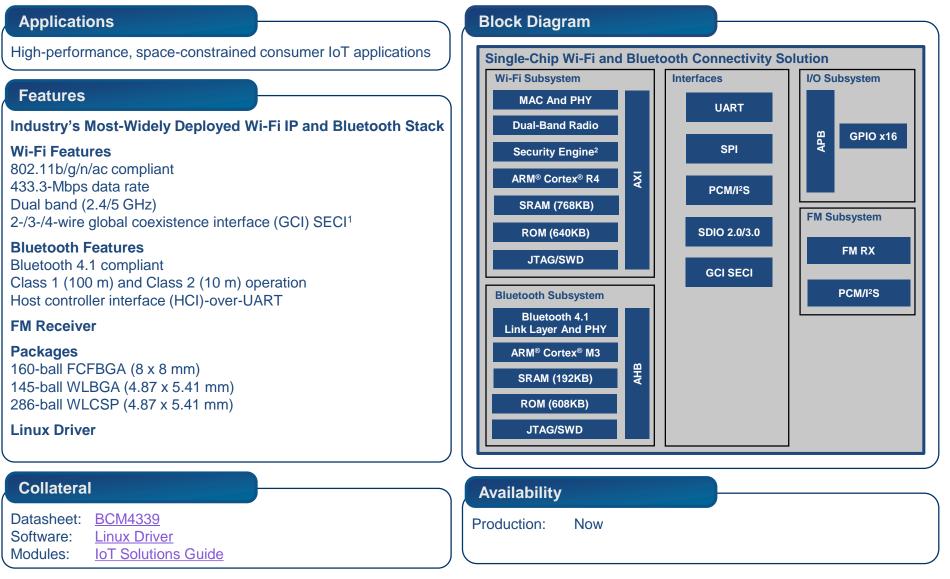




#### <sup>1</sup> WPA, WAPI STA, WPA2, AES, TKIP security features <sup>2</sup> Serial-enhanced coexistence interface

### Single-Chip IEEE 802.11ac with Integrated Bluetooth 4.1 and FM Receiver



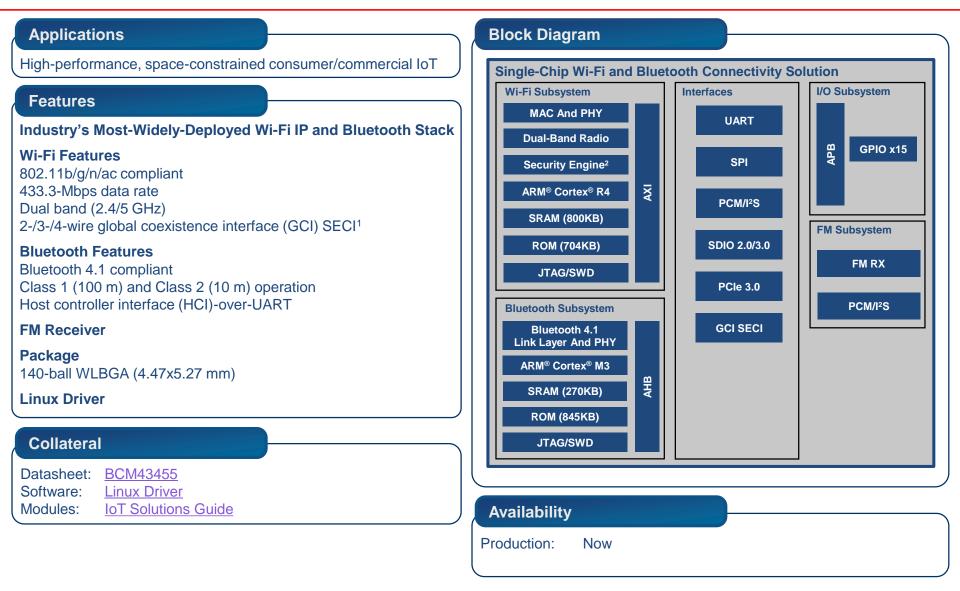


<sup>1</sup> Serial-enhanced coexistence interface

<sup>2</sup> WPA, WAPI STA, WPA2, AES, TKIP security features

### Single-Chip IEEE 802.11ac with Integrated Bluetooth 4.1 and FM Receiver





<sup>1</sup> Serial-enhanced coexistence interface

<sup>2</sup> WPA, WAPI STA, WPA2, AES, TKIP security features



# Wireless Solutions for The Internet of Things (IoT) Roadmap AUTOMOTIVE WIRELESS PORTFOLIO

## **Automotive Wireless Portfolio**



	Bluetooth (BR <sup>1</sup> + EDR <sup>2</sup> )	IEEE 802.11a/b/g/n/ac WLAN <sup>3</sup> + Bluetooth
- Performance/Integration	NEW       BCM89071         24-MHz ARM7TDMI-S4, SPI, UART, I²C,       I²S/PCM, GCI <sup>5</sup> SECI <sup>6</sup> ,         HCI <sup>7</sup> -over-UART/SPI,       8 GPIO, 112KB RAM,         BT <sup>8</sup> 4.1 BR + EDR, C1/C2/C3 <sup>9</sup> NEW       BCM20713         24-MHz ARM7TDMI-S, SPI, UART, I²C,         I*S/PCM, GCI SECI, HCI-over-UART/SPI,         8 GPIO, 16KB RAM,         BT 4.0 BR + EDR, C1/C2/C3	NEW         BCM89359         Q416           Up to 867 Mbps, 802.11a/b/g/n/ac,         2x2 MIMO <sup>10</sup> w/RSDB <sup>11</sup> , BT 4.2 BR + EDR + BLE,         GCI SECI, SDIO 3.0, PCIe, UART, USB, I <sup>2</sup> C,           SPI, HCI-over-UART, PCM/ <sup>2</sup> S, Security <sup>12</sup> ,         20 GPIO, C1/C2,         Linux Driver           NEW         BCM88335/BCM89359         Nup to 433.3 Mbps, 802.11a/b/g/n/ac, SISO <sup>13</sup> ,         BT4.1 BR + EDR + BLE, GCI SECI,           SDIO2.0/3.0,         SPI, HCI-over-UART, PCM/ <sup>12</sup> S, Security,         9 GPIO, C1/C2,         Linux Driver
1		Concept Development Sampling Production Status Availability

Cypress Roadmap: Wireless Solutions for The IoT

<sup>1</sup> Basic Rate

- <sup>2</sup> Enhanced Data Rate
- <sup>3</sup> Wireless Local Area Network
- <sup>4</sup> ARM 7 Family CPU
- <sup>5</sup> Global coexistence interface

- <sup>6</sup> Serial-enhanced coexistence interface
- <sup>7</sup> Host controller interface
- <sup>8</sup> Bluetooth Specification
- <sup>9</sup> Class 1 (100 m)/2 (10 m)/3 (1 m)
- <sup>10</sup> Multiple-input multiple-output

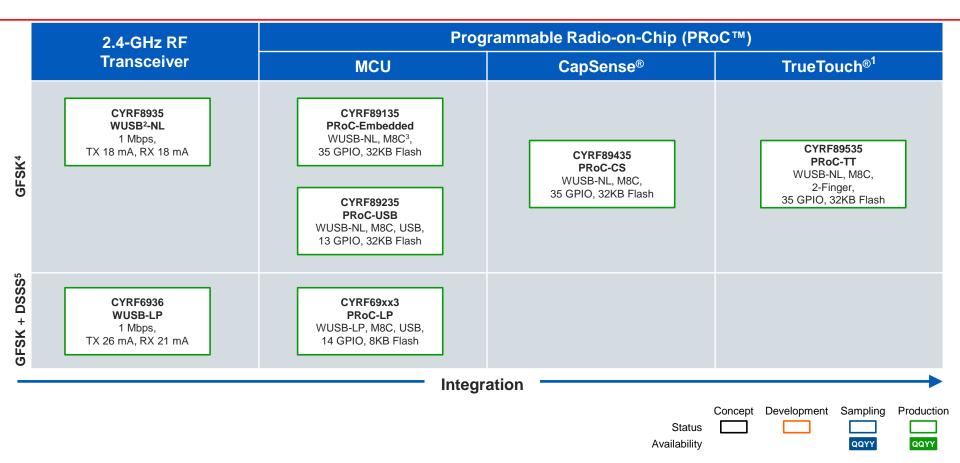
- <sup>11</sup> Real Simultaneous Dual Band
- <sup>12</sup> WPA, WAPI STA, WPA2, AES, TKIP, Cisco
- Compatible Extensions security features
- <sup>13</sup> Single-input single-output



### Wireless Solutions for The Internet of Things (IoT) Roadmap PROPRIETARY WIRELESS PORTFOLIO

## **Proprietary Wireless Portfolio**





<sup>1</sup> Touch-sensing technology with 2-finger gestures <sup>2</sup> WirelessUSB™ <sup>3</sup> Cypress proprietary 8-bit MCU

001-89683 Owner: SGUP Rev \*P BUM: MIHO <sup>4</sup> Gaussian frequency shift keying <sup>5</sup> Direct sequence spread spectrum

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